

# Workshop SCAPE 2019

13-14 May 2019, Radisson Blu Royal Park Hotell



## Tuesday 14 May

08:30 - 09:00 Registration & coffee

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### GaN Devices & Modules

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09:00 **Thierry Bouchet, Leti**  
*A new 650V GaN Half Bridge Power IC to Reach Ultimate USB Power Delivery Efficiency*

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09:20 **Marco Panizza, Panasonic**  
*Hybrid Drain GIT GaN based Totem Pole PFC Design - Reliability and circuital considerations*

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09:40 **Mattia Guacci, ETH**  
*Analysis of Monolithic Bidirectional GaN-switches and a New Modulation Scheme for Three-phase CSIs*

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10:00 - 10:30 Coffee & Exhibition

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### Resonant Convertors

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10:30 **Enrique Dede, Smart Induction Converter Technologies**  
*SiC Ultra Compact Smart Inverter Module for Induction Heating Applications*

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10:50 **John Kåre Langelid, EFD Induction**  
*Practical Experiences Using SiC DioMos in High Frequency High Power Series Resonant Applications*

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11:10 **Per Ranstad, KTH, Royal Institute of Technology**  
*On switching losses in a soft switching resonant converter*

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### SiC MOSFET Analysis

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11:30 **Andreas Huerner, Infineon**  
*Compact-Models for SiC-MOSFETs*

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11:50 **Amine Allouche, System Plus Consulting**  
*SiC Power MOSFETs State of the Art - Technology and Cost Overview*

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12:10 - 13:30 Lunch & Exhibition

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### Market & Roadmap

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13:30 **Hong Lin, Yole Développement**  
*GaN & SiC power devices - Market overview*

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### Panel Discussion

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14:00 **Chair: Hong Lin, Yole Développement**  
*Do we move towards wafer manufacturing integration & Can SiC production survive the automotive growth / boom*

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15:00 **Volker Sinhoff, KCEO AIXaTECH**  
*Low Temperature Epitaxy (LTE) - a corner stone for price parity between silicon and wide bandgap semiconductors*

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### Packaging

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15:20 **Kirill Klein, Fraunhofer IZM**  
*The Evolution of Low Inductive Power Modules*

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### Closing Remarks

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15:40 **Hans-Peter Nee, KTH, Royal Institute of Technology**

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16:00 End of workshop

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